

DATA PROCESSING  
SYSTEM 10

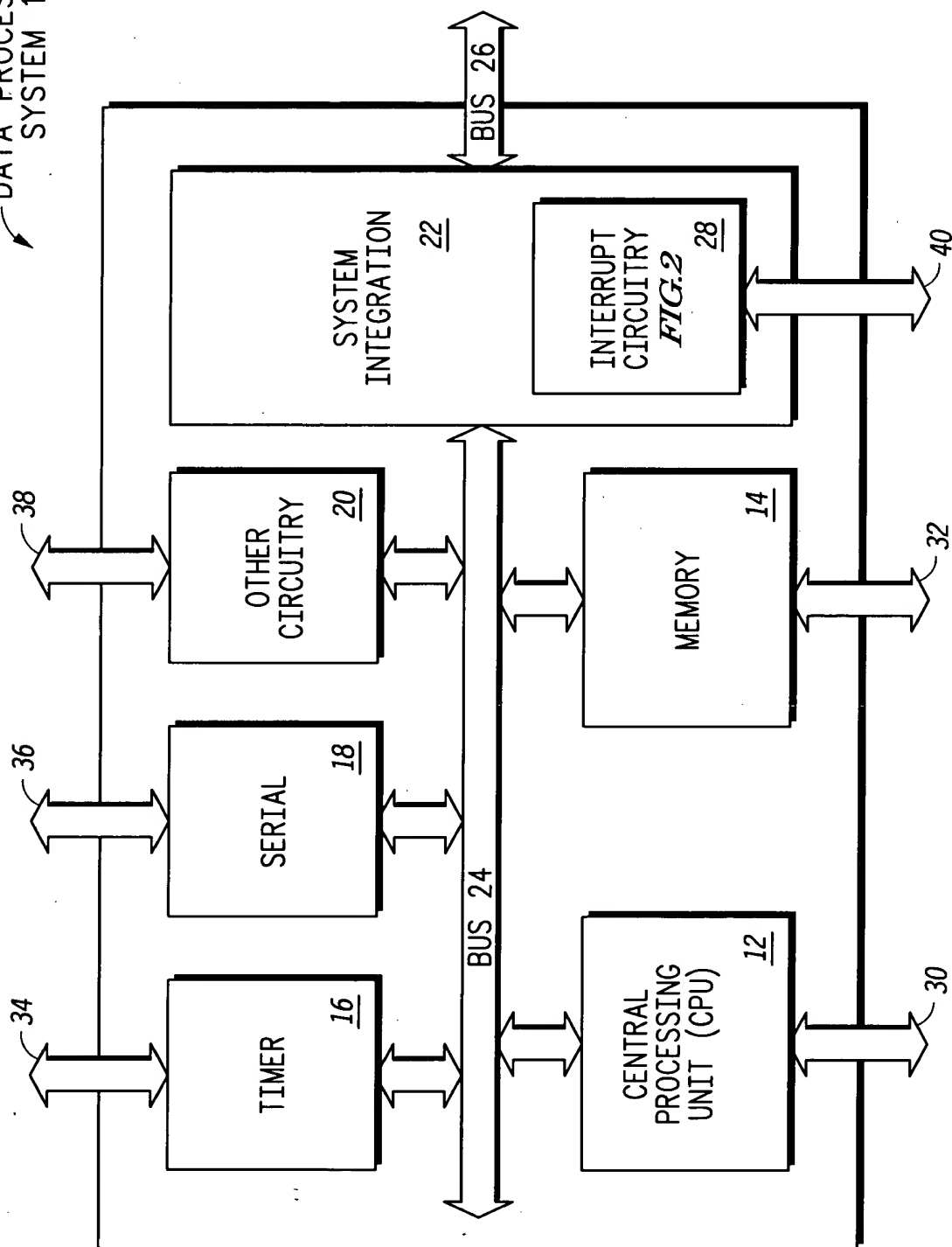


FIG. 1

09490132, 012400

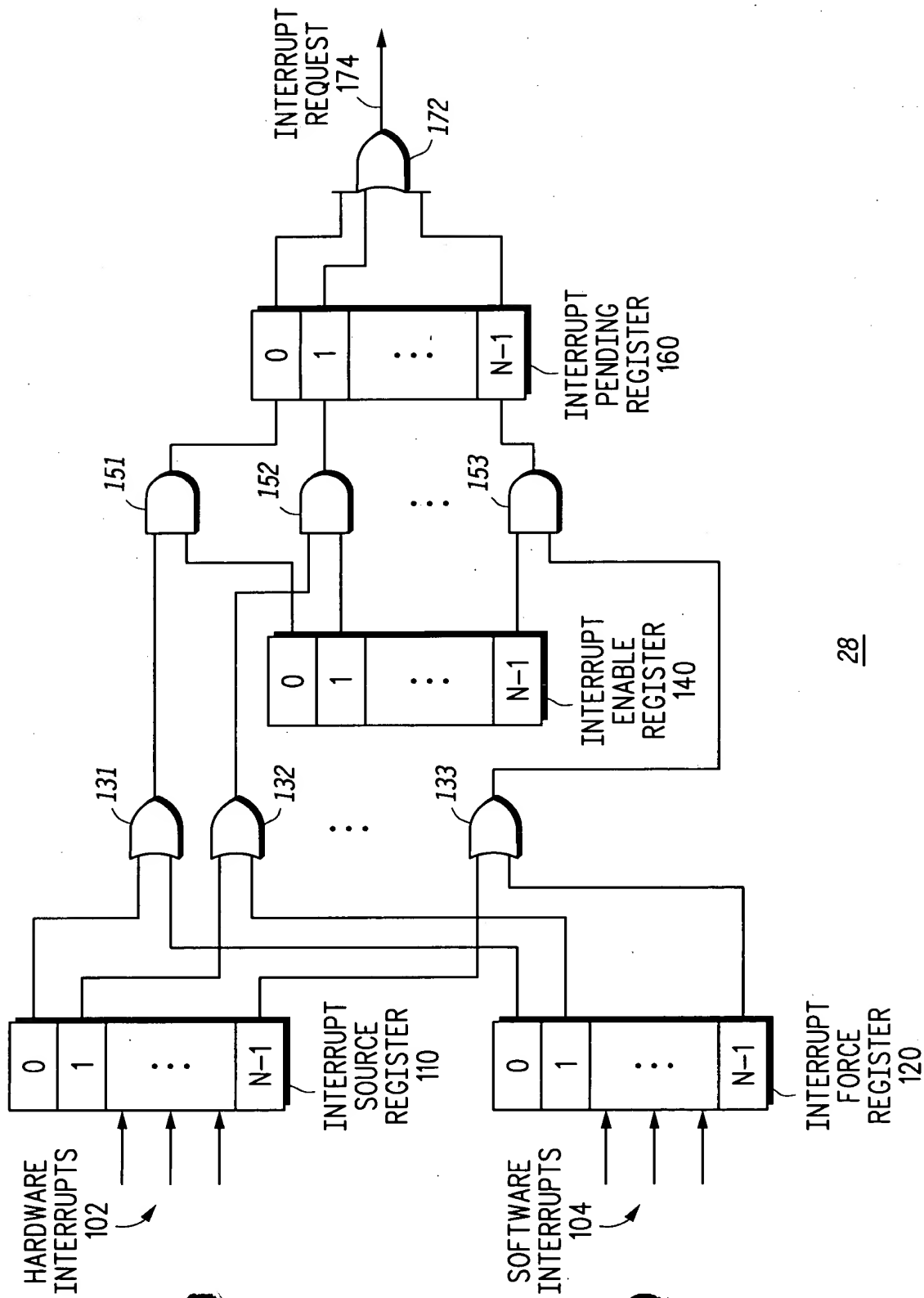


FIG.2

09490132.012400

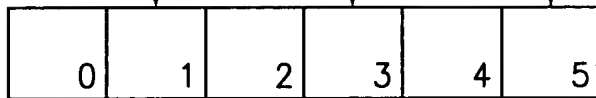
3/4

INTERRUPT  
SOURCE  
REGISTER  
200

PARALLEL  
I/O

TIMER

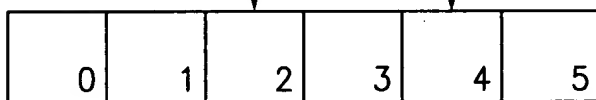
UART



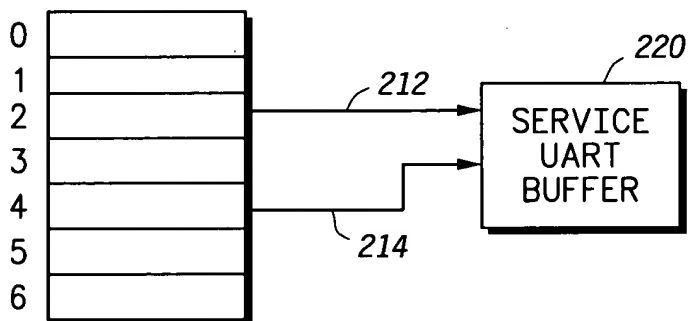
INTERRUPT  
FORCE  
REGISTER  
201

UART  
BUFFER  
LOW

UART  
BUFFER  
HIGH



**FIG. 3**



JUMP TABLE  
210

**FIG. 4**

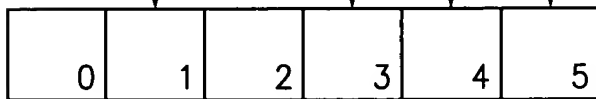
INTERRUPT  
SOURCE  
REGISTER  
300

PARALLEL  
I/O

TIMER

ALARM  
CLOCK

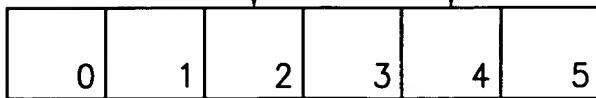
UART



INTERRUPT  
FORCE  
REGISTER  
301

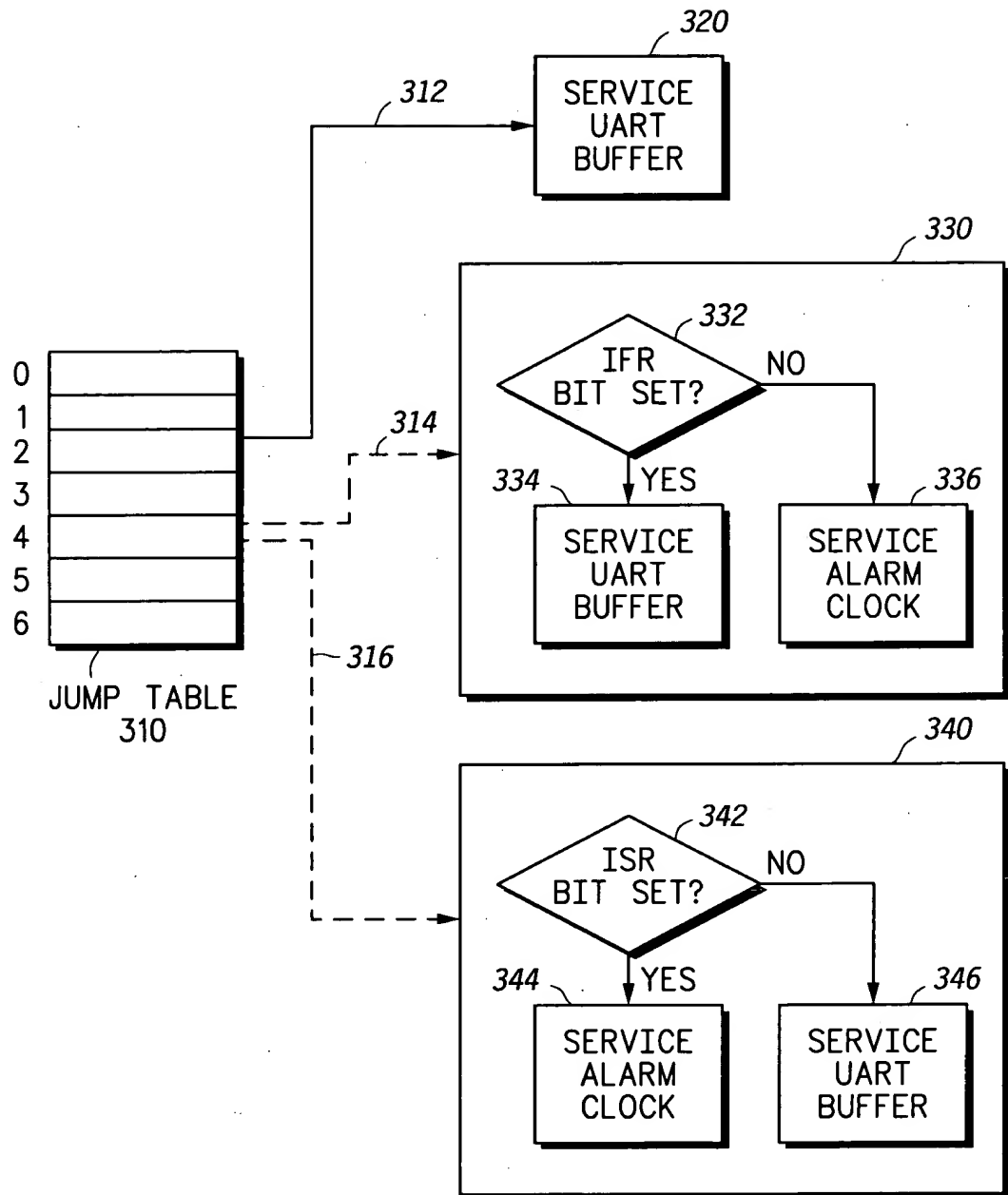
UART  
BUFFER  
LOW

UART  
BUFFER  
HIGH



**FIG. 5**

004270-22706160

**FIG. 6**